

# MicroEJ and VeriSilicon Join Forces to Accelerate Hardware IP Innovation, Thanks to Software Virtualization Leveraging 10 Million SW Engineers Worldwide

- MicroEJ Virtual Execution Environment (VEE) leverages VeriSilicon ViVante GCNanoLite-V graphical IP to provide User Interface Software Designers a no hassle access to Hardware innovations, such as vector graphics, Freetype fonts and smooth animations
- VeriSilicon and MicroEJ synergies complement the "Silicon Platform as a Service" (SiPaaS®) concept with the "write-Software-once, run-it-on-any-silicon-product", enabling an extraordinary fast path for electronic system definition, both for Hardware and Software, to test and package them in a short period of time
- This combined solution is ideal for Smart Home, Appliances and Wearables manufacturers willing to develop their own SoC or MCU, specifically tailored to their needs; it is also ideal for semiconductors seeking to offer an all-in-one SW+HW solution for sharp looking graphics for cost effective, electronic devices
- MicroEJ VEE leveraging GCNanoLite-V IP will be demonstrated on the i.MX-RT MCU from NXP at Embedded World 2020, in VeriSilicon booth 360, Halls 4A.

**Boston, February 24, 2020** – VeriSilicon, a Silicon Platform as a Service (SiPaaS) company, and MicroEJ, a leader in trusted virtual execution environments for cost effective SoC/MCU/MPU, today announces their collaboration on providing software and hardware IP, with related tools, to manufacturers and silicon players willing to develop their own SoC or MCU. Together, the two partners make it possible to run complex legacy software on new integrated VeriSilicon IP based hardware, thanks to secure virtualization provided by MicroEJ Virtual Execution Environment (VEE). As soon as the MicroEJ VEE secure container runs on the new chip, all the previous software assets run identically, transparently leveraging the new hardware innovation from VeriSilicon IP built in the new chip.

The Graphics Processing Unit ViVante GCNanoLite-V IP is the first IP that benefits from the partnership: MicroEJ VEE extendable embedded UI/UX open source multi-languages library (C, Java, JavaScript, etc.) enables you to run new and legacy UI applications, instantly. The first MCU powered by MicroEJ and VeriSilicon is a MCU from the i.MX-RT family by NXP, targeting wearable, appliances, home and industrial markets.

MicroEJ VEE bridges Software innovation with Hardware innovations, enabling full scalability from conception to the development of a whole new line of products, thanks to a joint software and hardware components mindset: assembling reusable assets, both software and hardware, thanks to secure virtualization. This solution is specifically well suited for very large volume markets, where

optimizing the Bill-Of-Material is a priority while still providing rich User Interface Experiences on resource constrained embedded devices.

*“With its incredible growth, the small electronics industry is increasingly looking for both low consuming GUI combined with impressive performance on a very low footprint. When NXP chose our Vivante GCNanoLite-V IP to build their next gen i.MX-RT MCU, MicroEJ VEE was the obvious natural one-device platform, as our combined technologies follow the same cost-oriented logic to address the same markets”* said Jarmon David, Sr. VP Worldwide Sales and Business Development at VeriSilicon.

*“Teaming with VeriSilicon to provide cutting edge graphical user interface libraries for impeccable looking user experiences with security at execution level, is the essence of MicroEJ VEE, often described as “the tiny sibling of Android” : making it as easy as possible for the mass numbers of the software engineers to leverage hardware innovation”* said Fred Rivard, PhD, CEO of MicroEJ.

A demo of ViVante GCNanoLite-V IP in a new i.MX-RT MCU from NXP is available here:  
[https://microej.nxp.com/Powered\\_by\\_MicroEJ\\_imxRT/](https://microej.nxp.com/Powered_by_MicroEJ_imxRT/)

## **About VeriSilicon**

VeriSilicon Microelectronics (Shanghai) Co., Ltd. (VeriSilicon) is committed to providing customers with platform-based, all-round, one-stop custom silicon services and semiconductor IP licensing services leveraging its in-house semiconductor IP. Under the unique "Silicon Platform as a Service" (SiPaaS) business model, depending on the comprehensive IP portfolio, VeriSilicon can create silicon products from definition to test and package in a short period of time, and provides high performance and cost-efficient semiconductor alternative products for IDM, Fabless, system vendors (OEM/ODM) and large Internet companies, etc. VeriSilicon's business covers consumer electronics, automotive electronics, computer and peripheral, industry, data processing, Internet of Things and other applications. VeriSilicon presents a variety of customized silicon solutions, including high-definition video, high-definition audio and voice, In-Vehicle Infotainment, video surveillance, IoT connectivity, data center, etc. In addition, VeriSilicon has five types of in-house processor IPs, namely GPU IP, NPU IP, VPU, DSP IP and ISP IP, and more than 1,400 analog and mixed signal IPs and RF IPs. Founded in 2001 and headquartered in Shanghai, China, VeriSilicon has 5 design and R&D centers in China and the United States, as well as 10 sales and customer service offices worldwide. VeriSilicon currently has more than 800 employees.

For more info: <http://www.verisilicon.com>

## **About MicroEJ**

MicroEJ is a software vendor of cost-driven solutions for embedded and IoT devices. We are focused on providing device manufacturers with secure products in markets where software applications require high performance, compact size, energy efficiency, and cost-effective development.

Today more than 120+ companies in the world with currently over 30 million products sold, have already chosen MicroEJ to design electronic product applications in a large variety of industries,

including smart home, wearables, healthcare, industrial automation, retail, telecommunications, smart city, building automation, transportation, etc.

For more info: [www.microej.com](http://www.microej.com)

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