

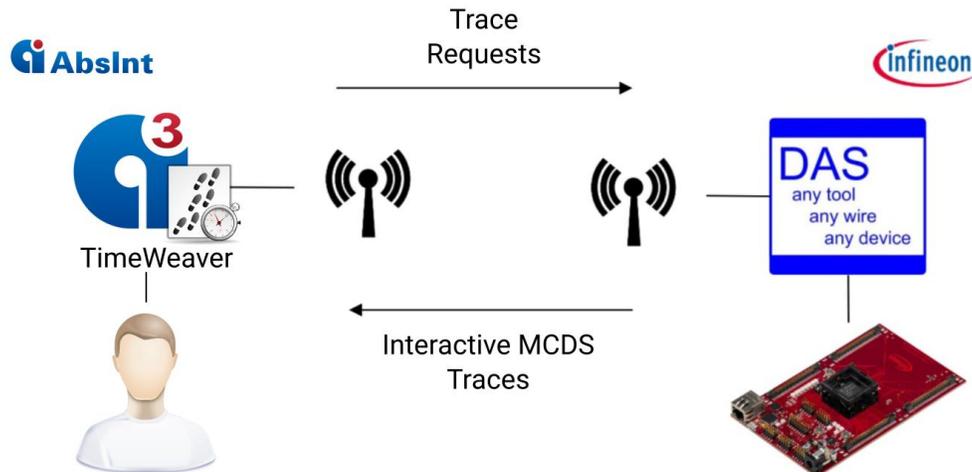
AbsInt and Infineon offer a new Timing Tool Chain for AURIX

Saarbrücken / Neubiberg, February 5th, 2019: AbsInt and Infineon Technologies have worked closely together. This cooperation has produced a new and extended version of AbsInt's TimeWeaver for Infineon 32-bit TriCore™ AURIX™ Microcontroller boards. TimeWeaver is a tool that estimates the worst-case execution time (WCET) of tasks/code snippets.

AbsInt, the specialist for program analysis tools for safety-critical software, can now offer direct support for Infineon AURIX™ family CPUs via the Device Access Server (DAS) tool infrastructure.

TimeWeaver combines static path analysis with timing measurements to estimate the worst-case execution time (WCET) of tasks based on the execution time of trace segments obtained from real-time instruction-level tracing. The computed time bounds are valuable for real-time systems and provide feedback for optimizing worst-case performance. TimeWeaver is non-intrusive. It needs no code instrumentation that distorts timing measurements.

The user just provides the application ELF binary, specifies a function/task entry and connects via USB/network to the Infineon DAS Trace Server directly talking to the Infineon AURIX™ Emulation Device hardware. The hardware sends the MCDS trace data back to TimeWeaver which computes a worst-case execution path and time, visualizes the path, and provides detailed reporting about timing contributions and trace coverage back to the user.



“This easy to use analysis environment supports our customers in developing safe hard real time systems. It is an outstanding example how AURIX™ powerful trace features can be used with a cost effective multi-tool hardware setup.”, said Albrecht Mayer, Senior Principal Emulation Systems and Tooling of Infineon.

“With our aiT tool we offer a pure static worst-case execution time analyzer for the timing-predictable AURIX™ cores. The hybrid TimeWeaver tool complements aiT nicely, as also the timing behavior of critical software running on multi-core processors can be analyzed efficiently.”, said Christian Ferdinand, CEO of AbsInt.

About AbsInt

AbsInt provides cutting-edge development tools for embedded systems with a focus on validation, verification, and certification of safety-critical software. AbsInt tools will ensure reliable and robust software that usually means less expensive and faster development cycles.

The company name is an acronym for “abstract interpretation”, a sophisticated approach to static program analysis on which many of the company’s highly successful products are based. Founded in 1998, AbsInt is a privately-held company in Saarbrücken, Germany.

For further information, visit www.absint.com.

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